APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date::	11-18-2003
Application Type::	Regular
Subject Matter::	Utility
Title::	Self-Repairing Redundancy for Memory Blocks in Programmable Logic Devices
Attorney Docket Number::	X-1363 US
Request for Early Pub?::	No
Request for Non-Pub?::	Yes
Total Drawing Sheets::	06
Small Entity?::	No
Petition included?::	No

APPLICANT INFORMATION

Applicant Authority Type::	Inventor
Primary Citizenship Ctry::	Canada
Status::	Full Capacity
Given Name::	Tony
Middle Name::	K.
Family Name::	Ngai
Street::	19058 Bonnet Way
City::	Saratoga
State or Province::	California
Postal or Zip Code::	95070

Applicant Authority Type::	Inventor
Primary Citizenship Ctry::	US
Status::	Full Capacity
Given Name::	Jennifer
Family Name::	Wong
Street::	40565 Encanto Way
City::	Fremont

State or Province::   California
Postal or Zip Code::   94539

Applicant Authority Type::   Inventor
Primary Citizenship Ctry::   US
Status::   Full Capacity
Given Name::   Wayson
Middle Name::   J.
Family Name::   Lowe
Street::   101 London Lane
City::   Belmont
State or Province::   California
Postal or Zip Code::   94002

CORRESPONDENCE INFORMATION

Correspondence Customer Number::   24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	

ASSIGNEE INFORMATION

Assignee Name::   Xilinx, Inc.
Street::   2100 Logic Drive
City::   San Jose
State or Province::   California
Postal or Zip Code::   95124